# METHODS AND SYSTEMS FOR MAINTAINING
# INFORMATION FOR LOCATING PROCESSOR INSTRUCTIONS

## ABSTRACT OF THE INVENTION

5      Methods and systems that allow recovery of the program counter or instruction pointer for a target (non-native) instruction that is translated into a host (native) instruction,  and that allow recovery of other information about the translator or the target system state, are described.  The program counter or instruction pointer can be recovered, for example, after an exception has been

10    processed or incident to a rollback operation.